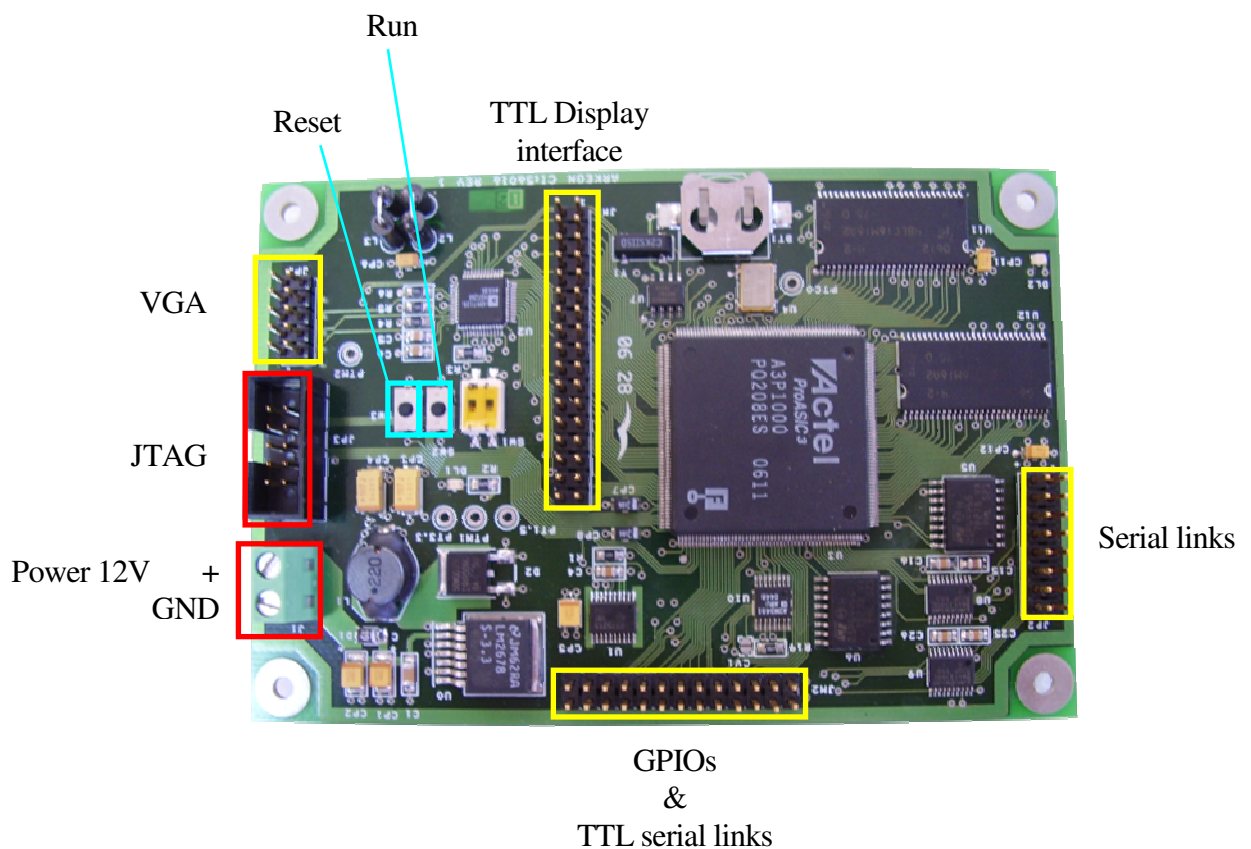


Advanced Electronic Design

Arkéon VGA



Caractéristiques

Composants	Connecteurs
Contrôleur : Bornéo Mémoire Flash : 16 MB Mémoire Programme : 32 MB Mémoire Vidéo : 32 MB 1 Real Time Clock (avec batterie)	4 RS232 1 RS422/485 14 GPIOs

Description Connecteurs

Serial links (JP2) :

Pin Name	I/O	Pin Number	Description
RS232_TX1	O	1	RS232 Transmit Data
RS232_RX1	I	2	RS232 Receive Data
GND	O	3	Ground
RS232_TX2	O	4	RS232 Transmit Data
RS232_RX2	I	5	RS232 Receive Data
GND	O	6	Ground
RS232_TX3	O	7	RS232 Transmit Data
RS232_RX3	I	8	RS232 Receive Data
GND	O	9	Ground
RS232_TX4	O	10	RS232 Transmit Data
RS232_RX4	I	11	RS232 Receive Data
GND	O	12	Ground
RS422 IN +	I	13	RS422 in +
RS422 IN -	I	14	RS422 in -
RS422 OUT +	O	15	RS422 OUT +
RS422 OUT -	O	16	RS422 OUT -

VGA (JP1) :

Pin Name	I/O	Pin Number	Description
VGA_VOR	O	1	Signal vidéo rouge
GND	O	2	Ground
VGA_VOG	O	3	Signal video vert
GND	O	4	Ground
VGA_ROB	O	5	Signal video bleu
GND	O	6	Ground
VGA_HSYNC	O	7	Signal synchro horizontal
GND	O	8	Ground
VGA_VSYNC	O	9	Signal synchro vertical
GND	O	10	Ground

GPIO (JM2) :

Pin Name	I/O	Pin Number	Description
VDD3.3V	O	1	VDD 3.3 Volts
VDD3.3V	O	2	VDD 3.3 Volts
FPGA_UNUSED0	IO	3	IO0
FPGA_UNUSED1	IO	4	IO1
FPGA_UNUSED2	IO	5	IO2
FPGA_UNUSED3	IO	6	IO3
FPGA_UNUSED4	IO	7	IO4
FPGA_UNUSED5	IO	8	IO5
FPGA_UNUSED6	IO	9	IO6
FPGA_UNUSED7	IO	10	IO7
FPGA_UNUSED8	IO	11	IO8
FPGA_UNUSED9	IO	12	IO9
FPGA_UNUSED10	IO	13	IO10

FPGA_UNUSED11	IO	14	IO11
FPGA_UNUSED12	IO	15	IO12
FPGA_UNUSED13	IO	16	IO13
RS232_RX3	I	17	RS232 Receive Data
RS232_TX3	O	18	RS232 Transmit Data
RS232_RX4	I	19	RS232 Receive Data
RS232_TX4	O	20	RS232 Transmit Data
FPGA_RS422_ROUT	I	21	RS422 OUT
FPGA_RS422_RE	O	22	RS422 RE
FPGA_RS422_DIN	O	23	RS422 DIN
FPGA_RS422_DE	O	24	RS422 DE
GND	O	25	Ground
GND	O	26	Ground

GPIO (JM1) :

Pin Name	I/O	Pin Number	Description
FPGA_DAC_R0	O	1	Rouge
FPGA_DAC_B0	O	2	Bleu
FPGA_DAC_R1	O	3	Rouge
FPGA_DAC_B1	O	4	Bleu
FPGA_DAC_R2	O	5	Rouge
FPGA_DAC_B2	O	6	Bleu
FPGA_DAC_R3	O	7	Rouge
FPGA_DAC_B3	O	8	Bleu
FPGA_DAC_R4	O	9	Rouge
FPGA_DAC_B4	O	10	Bleu
FPGA_DAC_R5	O	11	Rouge
FPGA_DAC_B5	O	12	Bleu
FPGA_DAC_R6	O	13	Rouge
FPGA_DAC_B6	O	14	Bleu
FPGA_DAC_R7	O	15	Rouge
FPGA_DAC_B7	O	16	Bleu
GND	O	17	Ground
GND	O	18	Ground
FPGA_DAC_G0	O	19	Vert
FPGA_DAC_SYNCH	O	20	Synchro DAC
FPGA_DAC_G1	O	21	Vert
FPGA_DAC_BLANK	O	22	Blank DAC
FPGA_DAC_G2	O	23	Vert
FPGA_DAC_CLOCK	O	24	Clock DAC
FPGA_DAC_G3	O	25	Vert
FPGA_DAC_PSAVE	O	26	PSave DAC
FPGA_DAC_G4	O	27	Vert
GND	O	28	Ground
FPGA_DAC_G5	O	29	Vert
GND	O	30	Ground
FPGA_DAC_G6	O	31	Vert
VDD1.5V	O	32	1.5 Volts
FPGA_DAC_G7	O	33	Vert
VDD3.3V	O	34	3.3 Volts

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